

EtherCAT Slave for CompactRIO

PIANY for cRIO [PI-7550]



LabVIEW FPGA Application

high speed (5/10kHz)

- ~200 bytes @200µs refresh rate
- ~100 bytes @100µs refresh rate

LabVIEW Real-Time Application

low speed (500Hz)

- >1000 network variables
- Event based synchronization

Connector

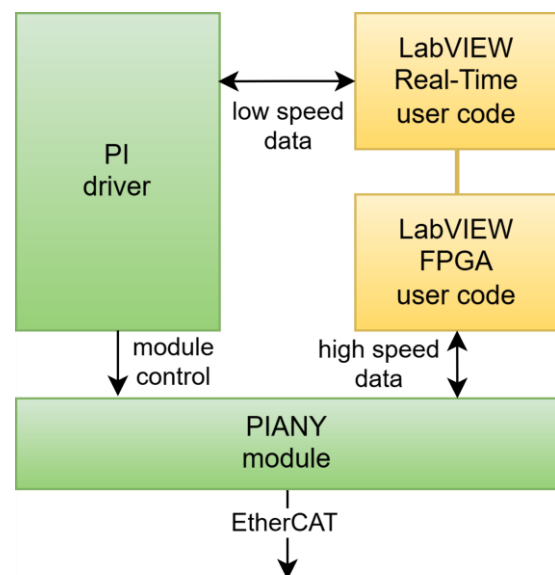
- 2x RJ45 connectors for EtherCAT
- External power supply, 9V...24V

Accessible Interfaces

- Definition of variables in table format
- Variables accessible on I/O interfaces

Applications

- High speed control loops on LabVIEW Target
- Extension of EtherCAT systems
- Connection of existing cRIO solutions to an EtherCAT network



Future communication bus standards

The existing architecture can be expanded to various bus types

- PROFINET IRT
- Modbus-TCP
- Ethernet POWERLINK
- CC-LINK IE Field
- BACnet/IP

ask for more information: info@pie.ch